AMENDMENTS TO THE DRAWINGS

Attached hereto is one (1) sheet of corrected formal drawings. The corrected formal drawings incorporate the following drawing changes:

In FIG. 1, the label "IDCT" in the block 102f has been replaced by the label "MC".

It is respectfully requested that the corrected formal drawings be approved and made a part of the record of the above-identified application.

Attachment: Replacement sheet

REMARKS

Applicant appreciates the Examiner's thorough consideration provided the present application. Claims 1-12 are now present in the application. The drawings and claims 1 and 2 have been amended. No new matter is added. Claims 11 and 12 have been added. Claim 1 is independent. Reconsideration of this application, as amended, is respectfully requested.

Priority Under 35 U.S.C. §119

Applicant thanks the Examiner for acknowledging Applicant's claim for foreign priority under 35 U.S.C. §119, and receipt of the certified priority document.

Drawings Objections

The drawings have been objected to due to the presence of minor informalities. Applicant has submitted one (1) sheet of corrected formal drawings to address the Examiner's requested changes. Accordingly, Applicant respectfully submits that this objection has been obviated and/or rendered moot. Reconsideration and withdrawal of this objection are respectfully requested.

Claim Rejections Under 35 U.S.C. §§ 102 & 103

Claim 1 stands rejected under 35 U.S.C. § 102(b) as being anticipated by Lane, U.S. Patent No. 5,598,222 (hereinafter "Lane") and Boyce et al., U.S. Patent No. 5,635,985 (hereinafter "Boyce") which is incorporated by reference in Lane. Claims 2-6, 8 and 10 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Lane and Boyce, in view of Lyu,

U.S. Patent Application Publication No. US 2001/0007576 (hereinafter "Lyu"). Claim 7 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Lane and Boyce in view of Lyu, and further in view of Yoon, U.S. Patent No. 6,226,039 (hereinafter "Yoon"). Claim 7 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Lane and Boyce in view of Lyu, and further in view of design choice. These rejections are respectfully traversed.

Complete discussions of the Examiner's rejections are set forth in the Office Action, and are not being repeated here.

In light of the foregoing amendments, Applicant respectfully submits that these rejections have been obviated and/or rendered moot. Without conceding to the propriety of the Examiner's rejections, but merely to timely advance the prosecution of the application, as the Examiner will note, independent claim 1 has been amended to recite a combination of elements including "a video decoder for receiving the video bitstreams of the plurality of channels from the transport decoders, and alternately decoding a plurality of video frames for the respective channels in a display frame period in the unit of a picture." Support for the above combination of elements set forth in claim 1 can be found in, e.g., FIGs. 6-7 and paragraphs [0046] and [0048] of the specification. Applicant respectfully submits that the above combination of elements set forth in claim 1 is not disclosed or suggested by the references relied on by the Examiner.

The Examiner referred to the dual MPEG-2 video decoder circuit 20 of Lane as the video decoder of claim 1. As shown in FIG. 1 of Lane, the video signals from the primary decoder circuit 12 and the secondary decoder circuit 14 are respectively sent to the full resolution video decoder 17 and the reduced resolution video decoder 19. However, Lane nowhere discloses that the full resolution video decoder 17 and the reduced resolution video decoder 19 operate *alternately* to decode the video signals from the primary decoder circuit 12 and the secondary

decoder circuit 14 in a display frame period. Therefore, Lane fails to teach "a video decoder

...*alternately decoding* a plurality of video frames for the respective channels in a display frame

period in the unit of a picture" as recited in claim 1.

Boyce also fails to cure the deficiencies of Lane. As the Examiner already knows, Lane

in col. 3, lines 47-50 states that the primary decoder circuit 12 and the secondary decoder circuit

14 may be implemented in accordance with the teachings in Boyce. In particular, as shown in

FIGs. 1 and 2A-C of Boyce, the bitstream I and the bitstream II are sent from the demodulators

102 to the decoder 201. The bitstream I represents the main picture data. The bitstream II then

is pre-parsed for use as the inset picture data (reduced-resolution data in the PIP). Boyce in col.

8, lines 55-60 further discloses:

> The PIP time slice controller 203 is synchronized with received video data
> and is responsible for the generation of the PIP signal which, in one embodiment,
> is used to <u>synchronize the processing of main and inset picture data</u> so that <u>both a
> main and inset picture is decoded during each respective frame time</u>. (Emphasis
> added).

In other words, the main picture data (bitstream I) and the inset picture data (reduced-resolution

bitstream II) are *synchronously*, not *alternately*, decoded at the same frame time due to the

synchronization. Therefore, Boyce also fails to teach "a video decoder ...*alternately decoding* a

plurality of video frames for the respective channels in a display frame period in the unit of a

picture" as recited in claim 1.

With regard to the Examiner's reliance on the secondary references, these references have

only been relied on for their teachings related to some dependent claims. These references also

fail to disclose the above combination of elements as set forth in amended independent claim 1.

Accordingly, these references fail to cure the deficiencies of Lane or Boyce.

Accordingly, none of the utilized references individually or in combination teach or suggest the above-noted features of amended independent claim 1. Therefore, Applicant respectfully submits that amended independent claim 1 and its dependent claims (at least due to their dependency) clearly define over the teachings of the utilized references. Accordingly, reconsideration and withdrawal of the rejections under 35 U.S.C. §§ 102 and 103 are respectfully requested.

Additional Claims

Claims 11 and 12 have been added for the Examiner's consideration. Applicant respectfully submits that claims 11 and 12 depend, either directly or indirectly, from independent claim 1, and are therefore allowable at least based on their respective dependence from independent claim 1. Favorable consideration and allowance of claims 11 and 12 are respectfully requested.

Additional Cited References

Since the remaining patents cited by the Examiner have not been utilized to reject the claims, but rather to merely show the state of the art, no further comments are necessary with respect thereto.

CONCLUSION

All the stated grounds of rejection have been properly traversed and/or rendered moot. Applicant therefore respectfully requests that the Examiner reconsider all presently pending rejections and that they be withdrawn.
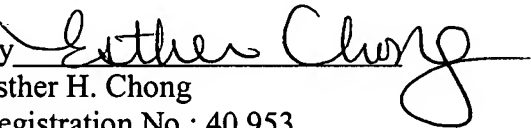
It is believed that a full and complete response has been made to the Office Action, and that as such, the Examiner is respectfully requested to send the application to Issue.

In the event there are any matters remaining in this application, the Examiner is invited to contact the undersigned at (703) 205-8000 in the Washington, D.C. area.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Dated: June 27, 2007

Respectfully submitted,

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Attachment: Replacement sheet